

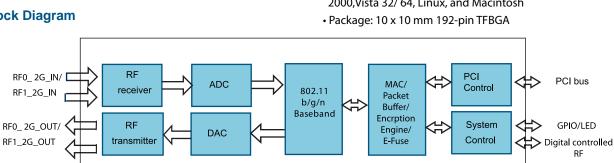
RT3062

- CMOS single chip
- Blazing speed at 300 Mbps PHY rate
- Low power



The RT3062 single chip is highly integrated with MAC/BBP, and operates at 2.4 GHz RF. It fully complies with IEEE 802.11n and IEEE 802.11 b/g, feature rich wireless connectivity at high standards, and deliver reliable, cost-effective throughput from an extended distance. Optimized RF architecture and baseband algorithms provide superb performance with low power consumption. An intelligent MAC design deploys a highly efficient PCI engine, and hardware data precessing acceleators without overloading the host processor. The RT3062 is designed to support basic features related to security, quality of service and international regulations. It gives end users the greatest performance at anytime, and in any circumstance.

Block Diagram



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Features

- PCI interface
- · Built in e-fuse memory
- 2T2R with 300Mbps PHY rate
- 20MHz/40MHz bandwidth support
- Legacy and High Throughput modes
- Reverse Data Grant (RDG)
- Maximum Likelihood Decoding (MLD)
- Maximal Ratio Combining (MRC)
- Security: WEP / TKIP / AES / WPA / WPA2 / WAPI
- WPS PIN, PBC
- QoS WMM, WMM-PS
- International Regulation 802.11 d+h
- Multiple BSSID support
- Cisco CCX5 support
- Bluetooth Co-existence
- MIMO power saving support
- Supported Operating Systems: Windows XP 32/64, 2000, Vista 32/64, Linux, and Macintosh

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