



ATHEROS
COMMUNICATIONS

AR5002AP-G

802.11b/g WLAN Solution

Provides an 802.11b/g solution for 2.4 GHz data-centric access points and home gateways.



AR5002AP-G Solution Highlights

- Support for IEEE 802.11b, 802.11g
- Uses digital CMOS technology exclusively, minimizing power consumption and cost while maximizing reliability
- Highly integrated 2-chip set
- 2.4 GHz Radio-on-a-Chip (RoC)
- Wireless System-on-a-Chip (WiSoC), including integrated 32-bit MIPS R4000-class processor and multiprotocol MAC/baseband processing engine that supports the RoC
- Wireless Multimedia Enhancements Quality of Service support (QoS)
- Super G™ mode delivers 108 Mbps raw data rate and 90 Mbps TCP/IP throughput
- Hardware encryption for the Wi-Fi Protected Access (WPA) and IEEE 802.11i security specifications – provides Advanced Encryption Standard (AES), Temporal Key Integrity Protocol (TKIP) and Wired Equivalent Privacy (WEP) without performance degradation
- Support for draft IEEE 802.11e, h, and i standards
- Third-generation OFDM radio provides best-in-class range, throughput and power consumption

AR2112 Radio-on-a-Chip for 2.4 GHz WLAN

- Support for IEEE 802.11b, 802.11g
- Operates from 2.300 - 2.500 GHz
- Advanced wideband receiver with best path sequencer for better range and multipath resistance than conventional equalizer-based designs
- Integrated third-generation power amplifier (PA) and low-noise amplifier (LNA)
- External PA and/or LNA can be used for special applications
- Eliminates all IF filters and most RF filters; no external voltage-controlled oscillators (VCOs) or surface acoustic wave (SAW) filters needed
- Enhanced transmit and receive chains

AR2312 Wireless System-on-a-Chip

- Integrated 32-bit MIPS R4000-class processor
- Wireless MAC and baseband processing engine
- Super A/G™ mode includes dynamic 108 Mbps capability, real-time hardware data compression, dynamic transmit optimization and standards-compliant bursting
- 10/100 Ethernet MACs
- High speed UART
- 16-bit configurable local bus
- Integrated analog-to-digital and digital-to-analog converters
- SDRAM and FLASH memory interface
- Low power operational and sleep modes

AR5002AP-G WLAN System Architecture

